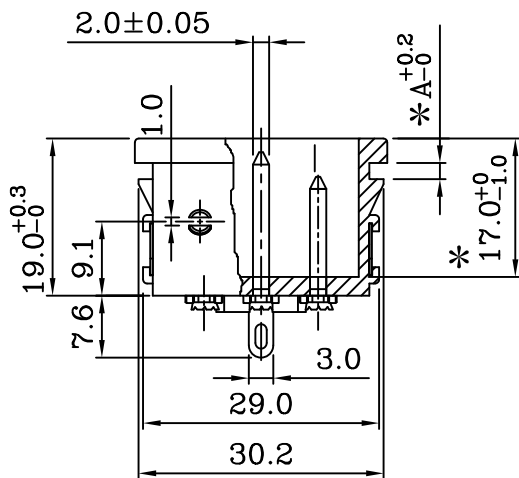
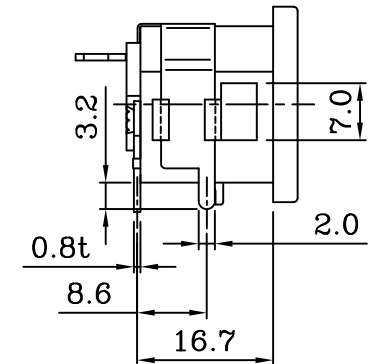
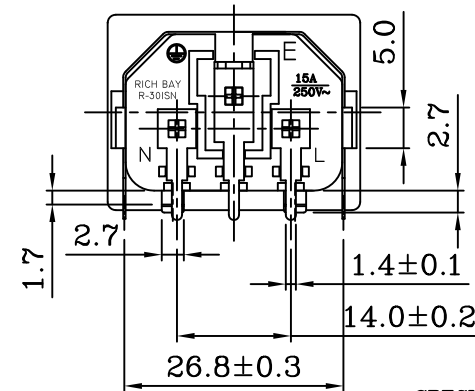
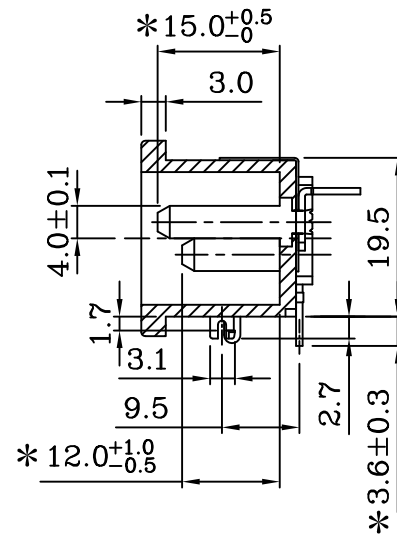
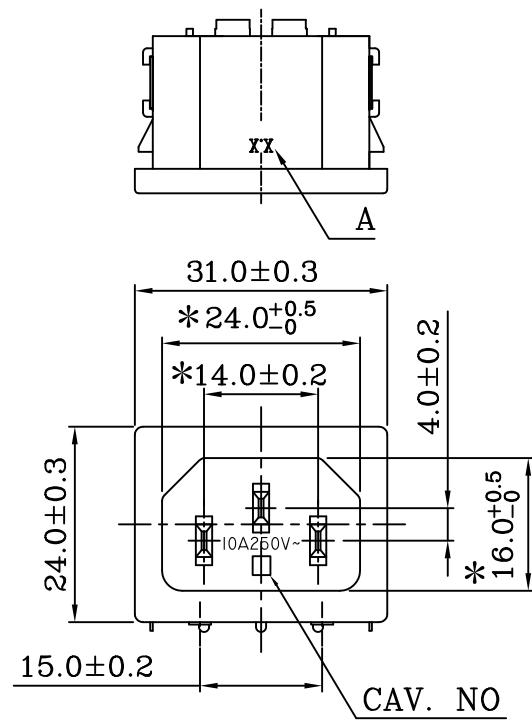
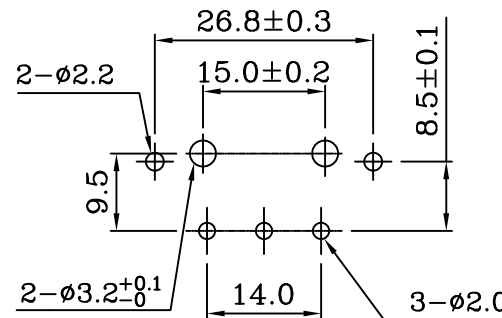




XX	
A	間隙
15	1.5 ^{+0.2} ₀
20	2.0 ^{+0.2} ₀



PCB LAYOUT

SPECIFICATION:

1. INSULATION RESISTANCE: OVER 100MΩ 500V DC.
2. DIELECTRIC STRENGTH: 2000V AC ONE MINUTE.
3. Body Material: Black Glass-Filled Thermoplastic . UL 94V-0~2

Rating:AC 15A 250V	
Mark	Safety Regulation
cULus	cULus
Rating:AC 10A 250V	
Mark	Safety Regulation
△	VDE
@	CCC
EN10	ENEC

JACKSON ELECTRONICS IND. CORP.
捷森電子工業股份有限公司



DRAWN BY

李
2019.08.27
小英

APPROVED BY

朱
2019.08.27
显德

TOLERANCE

X.XX±0.1
XX.XX±0.2
XXX.XX±0.3

PRODUCT NO.

JR-101S-B216

REV

ID NO

19082701Q

INTERIM NO.

DESCRIPTION

NEW NO.

C